# IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Leonard Forbes et al.

Examiner: Unknown

Serial No.:

Unknown

Group Art Unit: Unknown

Filed:

Herewith

Docket: 303.324US2

Title:

Ę

L m

M

TŲ

TRANSISTOR WITH VARIABLE ELECTRON AFFINITY GATE AND

METHODS OF FABRICATION AND USE

## PRELIMINARY AMENDMENT

**Assistant Commissioner for Patents** Washington, D.C. 20231

When the above-identified patent application is taken up for consideration, please amend the application as follws:

### IN THE SPECIFICATION

On page 1, line 7, before "The present invention" please insert the sentence application is a divisional of U.S. Serial No. 08/903,452 filed July 29, 1997.--

## IN THE CLAIMS

Please cancel claims 1-20 without prejudice.

#### REMARKS

Claims 1-20 are canceled, hereby, claims 21-35 are now pending in the application.

Respectfully submitted,

LEONARD FORBES ET AL.

By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

Minneapolis, MN 55402

(612) 373-6904

Date 7els. 23

Daniel J. Kluth

Reg. No. 32,146

CERTIFICATE UNDER 37 CFR 1.10:

"Express Mail" mailing label number: EM473004928US

Date of Deposit: February 23, 1999

I hereby certify that this paper or fee is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

Chris Hammond

Name